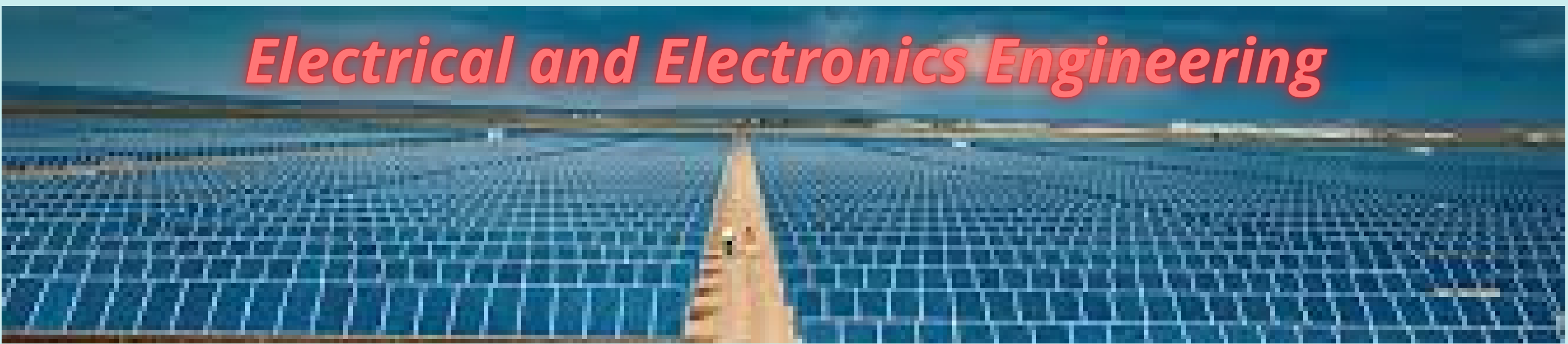




# Gayatri Vidya Parishad

## College of Engineering for Women

Approved by AICTE, New Delhi, Affiliated to JNTUK Kakinada



### Electrical and Electronics Engineering

#### VISION

To develop into a centre of learning that empowers students with contemporary knowledge in Electrical and Electronics Engineering for their overall growth.

#### MISSION

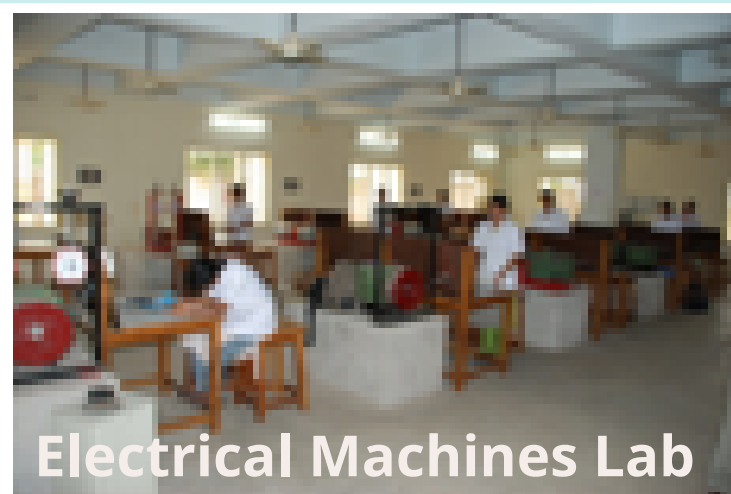
- Impart skills both in traditional and modern areas of Electrical and Electronics Engineering.
- Provide exposure to latest developments in the field through Seminars, Industrial visits, Workshops and Paper presentations.
- Prepare the young minds to apply professional engineering practices by considering environmental and societal needs.

- 30 % Faculty are Ph.D. holders
- 30 % faculty are pursuing Ph.D.
- Faculty have published 30 plus research articles in reputed journals and conferences
- All faculty obtained NPTEL online certifications and 30 % faculty are in Top 1 %

#### Thrust Areas of Faculty

1. Power Systems and Automation
2. Power Electronics and Drives
3. Optimization Techniques
4. Distribution Systems
5. Integration of Renewable Energy Sources to Micro Grids

### LABORATORIES



Electrical Machines Lab



Thermal & Hydro Lab



Basic Electrical Engg. Lab



Power Electronics & Control Systems Lab

Department Advisor  
Sastry V. Vedula  
Retd. Professor  
from IIT Chennai



Solar Tree in The Campus



Campus Hybrid Power Plant

## Our Alumni are Working in



#### Department Association (TESLA) Activities



#### Industrial Visits



#### IEEE Activities



#### Distribution of Merit Certificates





# BEST PRACTICES

ALUMNI



AMAZON



VIZAG STEEL PLANT



TCS



INFOSYS



INDIAN RAILWAYS



WIPRO



TECH MAHINDRA



DIGITAL ASSISTANT, AP Govt.



SONATA



WIPRO



COGNIZANT



IBeON INFOTECH



ACCENTURE



MAHILA POLICE



BANK of BARODA



TCS

## Department Association Activities (TESLA)

### Guest Lectures



Energy Audit & Conservation



Industrial Connect

### Workshops



Solar MPPT Techniques



Integration of RES to GRID

### Student Activities: Poster/Paper/Model Exhibition



Model Presentation



Poster Presentation

## Industrial Exposure & Co-Curricular Activities

### Industrial Visits



APEPDCL 33kV / 11 kV Substation

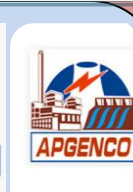


Vemagiri (400/220KV) Substation, Kalapaka



HVDC Substation, Gajuwaka,

### Internships



### Participation in Institutes of Repute



Workshop on "Recent Trends in Power Electronics" at NIT Raipur



Workshop on "PCB Design" at IIT Madras



## Learning Management System



## Virtual Labs



## Certification Courses



## Value Added Courses

- MATLAB
- PYTHON PROGRAMMING
- OPTIMIZATION TECHNIQUES
- RENEWABLE ENERGY

## Student Counselling



## Extra-Curricular Activities



## MOU's

